

PATENT APPLICATION FEE DETERMINATION RECORD Effective October 1, 2003

Application or Docket Number

EM C2-150PUS

CLAIMS AS FILED - PART I

	(Column 1)	(Column 2)
TOTAL CLAIMS	16	
FOR	NUMBER FILED	NUMBER EXTRA
TOTAL CHARGEABLE CLAIMS	16 minus 20 = *	-
INDEPENDENT CLAIMS	4 minus 3 = *	1
MULTIPLE DEPENDENT CLAIM PRESENT <input type="checkbox"/>		

* If the difference in column 1 is less than zero, enter "0" in column 2

CLAIMS AS AMENDED - PART II

	(Column 1)	(Column 2)	(Column 3)
AMENDMENT A	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA
Total	*	Minus	**
Independent	*	Minus	***
FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM <input type="checkbox"/>			

	(Column 1)	(Column 2)	(Column 3)
AMENDMENT B	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA
Total	*	Minus	**
Independent	*	Minus	***
FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM <input type="checkbox"/>			

	(Column 1)	(Column 2)	(Column 3)
AMENDMENT C	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA
Total	*	Minus	**
Independent	*	Minus	***
FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM <input type="checkbox"/>			

* If the entry in column 1 is less than the entry in column 2, write "0" in column 3.
 ** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, enter "20."
 *** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, enter "3."
 The "Highest Number Previously Paid For" (Total or Independent) is the highest number found in the appropriate box in column 1.

SMALL ENTITY TYPE ☐

OR OTHER THAN SMALL ENTITY

RATE	FEE	RATE	FEE
BASIC FEE	385.00	BASIC FEE	770.00
XS 9=		XS18=	
X43=		X86=	86
+145=		+290=	
TOTAL		TOTAL	856

SMALL ENTITY OR

OTHER THAN SMALL ENTITY

RATE	ADDITIONAL FEE	RATE	ADDITIONAL FEE
XS 9=		XS18=	
X43=		X86=	
+145=		+290=	
TOTAL ADDIT. FEE		TOTAL ADDIT. FEE	

RATE	ADDITIONAL FEE	RATE	ADDITIONAL FEE
XS 9=		XS18=	
X43=		X86=	
+145=		+290=	
TOTAL ADDIT. FEE		TOTAL ADDIT. FEE	

RATE	ADDITIONAL FEE	RATE	ADDITIONAL FEE
XS 9=		XS18=	
X43=		X86=	
+145=		+290=	
TOTAL ADDIT. FEE		TOTAL ADDIT. FEE	

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. :10/728,262
 Applicant :Jinhua Chen et al.
 Filed :December 4, 2003
 T.C./A.U. :2816
 Examiner :Nguyen, Linh M
 Docket No. :EMC2-150PUS
 Customer No. :45456

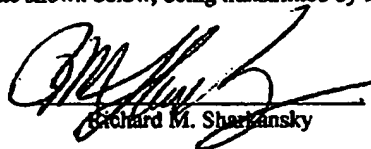
Confirmation No.:7078

CERTIFICATE OF MAILING/TRANSMISSION (37 C.F.R. § 1.8(a))

I hereby certify that this correspondence is, on the date shown below, being transmitted by facsimile to the Patent and Trademark Office at 703-872-9306

Date

3-18-2005


 Richard M. Sharkansky

RESPONSE

Mail Stop Amendment
 T.C./A.U. 2816
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

Sir:

This is in response to the Office Action mailed February 23, 2005. Please amend the above-identified application as follows:

03/23/2005 DSHALLS 00000002 050689 10728262

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Application No.: 10/728,262
Reply to Office Action of February 23, 2005

REMARKS/ARGUMENTS

The above-identified patent application has been amended and reconsideration and re-examination are hereby requested.

Claims 1-3, and 11-15 stand rejected as being either anticipated by Doblar et al, (U. S. Patent No. 6,640,309) or obvious over Lundberg (U. S. Pub. No. 204/086061). The Examiner takes the position that Doblar et al, describe a method wherein clock signals having either single- ended clock pulses or differential clock pulses are converted into clock signals having substantially the same voltage swing. It is respectfully submitted that such description does not appear in Doblar et al. Doblar et al, convert a differential signal into a single ended signal; however, Doblar et al, DO NOT describe a method wherein clock signals having either single- ended clock pulses or differential clock pulses are converted into clock signals having substantially the same voltage swing. Reconsideration is hereby requested.

Claim 4 has been amended to point out that the first and second voltage divider networks produce the same differential voltage swing for both single-ended or differential clock source signals voltage at the inverting and non-inverting input. Such is not described in Doblar et al.

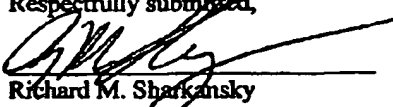
Claim 8 points out a transmission line is coupled between a source of clock signals and the input terminals, and wherein such transmission line has a characteristic impedance Z_0 , and wherein $R1 \cdot R2 / (R1 + R2)$ equals Z_0 . The Examiner takes the position that such a transmission line is inherent in Lundberg. It is respectfully submitted that such a transmission line is not described in, nor is such a transmission line inherent in, Lundberg. . Reconsideration is hereby requested.

In the event any additional fee is required, please charge such amount to Patent and Trademark Office Deposit Account No. 05-0889.

Date

3-18-2005

Respectfully submitted,


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